

Attorney Docket No: 0756-2288
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI

Serial No. 09/832,844

Filed: April 12, 2001

For: ELECTRO-OPTICAL DEVICE

) Group Art Unit: 2826

701 JAN -4 71 15

) Examiner: M. Tran

US PATENT & TRADEMARK
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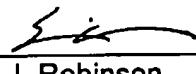
Dear Sir:

Applicants respectfully request a refund in connection with the above-identified patent application.

Applicants submitted an *Information Disclosure Statement* on September 21, 2005, along with a *Request for Continued Examination*. In light of the *Request for Continued Examination*, a fee is not required, therefore Applicant is entitled to a refund.

It is respectfully requested that the payment for the *Information Disclosure Statement* in the amount of \$180.00 be credited to Deposit Account No. 50-2280 (0756-2288). Should any additional information be needed, please do not hesitate to contact Applicants' undersigned representative at the number listed below.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.
PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789

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Deposit Account Statement

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Attorney Docket No. 0756-2288

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Serial No. 09/832,844
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) Group Art Unit: 2826
) Examiner: M. Tran
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Donald M. Stamps

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The cited references were submitted to the Japanese Patent Office in connection with the features of "the analog switch" and "the CMOS transmission gate." In particular, the *Semiconductor Handbook*, Basic Circuit of Analog Switch, describes an analog switch circuit using the CMOS transmission gate shown in Fig. 11.65(d). The analog switch circuit of Fig. 11.65(d) drives the switch consisting of Tr3 and Tr4, by providing a pair of gate pulses each of which has opposite polarity to the other. The pair of gate pulses are provided by inputting the gate input pulse to the inverter circuit consisting of Tr1 and Tr2. English translations of the drawings of the reference are shown on the copy of the reference by manuscript.

09/27/2005 DEMMANU1 00000002 502280 09832844

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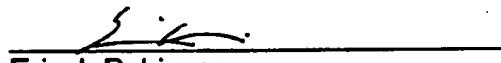
Adjustment Date: 03/20/2006 SDIRETA1
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This Information Disclosure Statement is being submitted after the issuance of an non-final Office Action, therefore, the \$180 fee is enclosed.

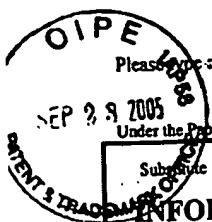
The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280. A duplicate copy of this sheet is attached.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	09/832,844
Filing Date	April 12, 2001
First Named Inventor	Shunpei YAMAZAKI
Group Art Unit	2826
Examiner Name	M. Tran
Attorney Docket Number	0756-2288

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		H. YANAI, 3.3.2 <i>Basic Circuit of Analog Switch</i> , Semiconductor Handbook, 2 nd Edition, Pages 842-843, 1977.	Concise Statement

Examiner Signature		Date Considered	
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***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in
use. Initial if reference not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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